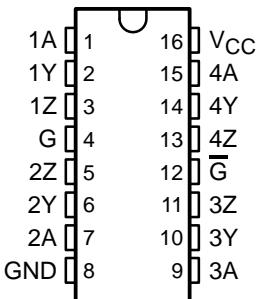


- Meets or Exceeds the Requirements of ANSI TIA/EIA-422-B and ITU Recommendation V.11
- Operates From a Single 5-V Supply
- TTL Compatible
- Complementary Outputs
- High Output Impedance in Power-Off Conditions
- Complementary Output-Enable Inputs

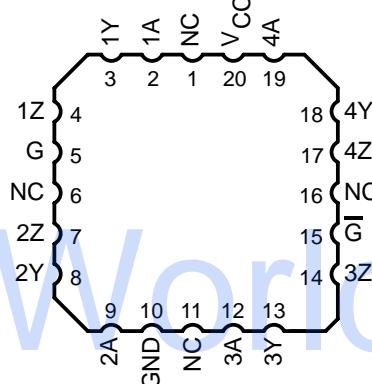
**D, DB, N, NS, OR J PACKAGE
(TOP VIEW)**



description/ordering information

The AM26LS31 is a quadruple complementary-output line driver designed to meet the requirements of ANSI TIA/EIA-422-B and ITU (formerly CCITT) Recommendation V.11. The 3-state outputs have high-current capability for driving balanced lines such as twisted-pair or parallel-wire transmission lines, and they are in the high-impedance state in the power-off condition. The enable function is common to all four drivers and offers the choice of an active-high or active-low enable (G , \bar{G}) input. Low-power Schottky circuitry reduces power consumption without sacrificing speed.

**FK PACKAGE
(TOP VIEW)**



ORDERING INFORMATION

TA	PACKAGE†		ORDERABLE PART NUMBER	TOP-SIDE MARKING
0°C to 70°C	PDIP – N	Tube	AM26LS31CN	AM26LS31CN
	SOIC – D	Tube	AM26LS31CD	AM26LS31C
		Tape and reel	AM26LS31CDR	
	SOP – NS	Tape and reel	AM26LS31CNSR	26LS31
–55°C to 125°C	SSOP – DB	Tape and reel	AM26LS31CDBR	SA31C
	CDIP – J	Tube	AM26LS31MJ	AM26LS31MJB
	LCCC – FK	Tube	AM26LS31MFK	AM26LS31MFKB

† Package drawings, standard packing quantities, thermal data, symbolization, and PCB design guidelines are available at www.ti.com/sc/package.



Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.

PRODUCTION DATA information is current as of publication date. Products conform to specifications per the terms of Texas Instruments standard warranty. Production processing does not necessarily include testing of all parameters.



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On products compliant to MIL-PRF-38535, all parameters are tested unless otherwise noted. On all other products, production processing does not necessarily include testing of all parameters.

AM26LS31C, AM26LS31M QUADRUPLE DIFFERENTIAL LINE DRIVER

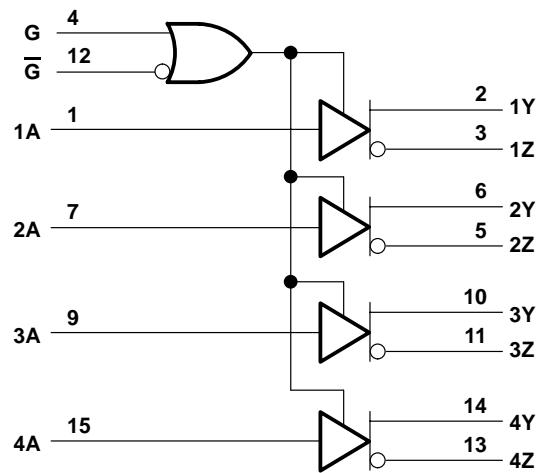
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FUNCTION TABLE
(each driver)

INPUT A	ENABLES		OUTPUTS	
	G	\bar{G}	Y	Z
H	H	X	H	L
L	H	X	L	H
H	X	L	H	L
L	X	L	L	H
X	L	H	Z	Z

H = high level, L = low level, X = irrelevant,
Z = high impedance (off)

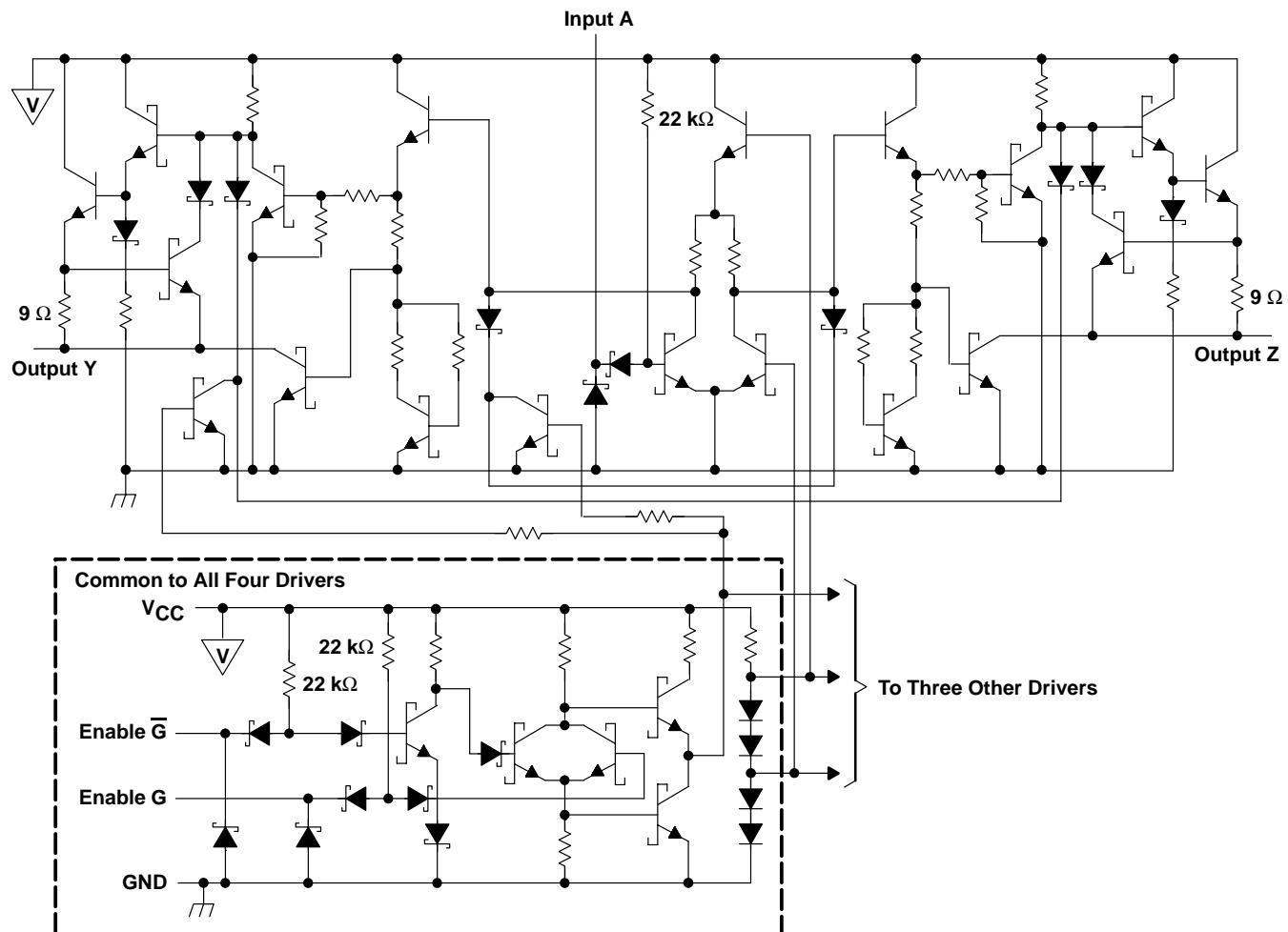
logic diagram (positive logic)



AM26LS31C, AM26LS31M
QUADRUPLE DIFFERENTIAL LINE DRIVER

SLLS114H – JANUARY 1979 – REVISED JULY 2002

schematic (each driver)



All resistor values are nominal.

AM26LS31C, AM26LS31M QUADRUPLE DIFFERENTIAL LINE DRIVER

SLLS114H – JANUARY 1979 – REVISED JULY 2002

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)[†]

Supply voltage, V _{CC} (see Note 1)	7 V
Input voltage, V _I	7 V
Output off-state voltage	5.5 V
Package thermal impedance, θ _{JA} (see Note 2): D package	73°C/W
DB package	82°C/W
N package	67°C/W
NS package	64°C/W
Lead temperature 1.6 mm (1/16 inch) from case for 10 seconds	260°C
Lead temperature 1.6 mm (1/16 inch) from case for 60 seconds: J package	300°C
Storage temperature range, T _{STG}	-65°C to 150°C

[†] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

- NOTES: 1. All voltage values, except differential output voltage V_{OD}, are with respect to network GND.
2. The package thermal impedance is calculated in accordance with JEDEC 51-7.

DISSIPATION RATING TABLE

PACKAGE	T _A ≤ 25°C POWER RATING	DERATING FACTOR ABOVE T _A = 25°C [‡]	T _A = 70°C POWER RATING	T _A = 125°C POWER RATING
FK	1375 mW	11.0 mW/°C	880 mW	275 mW
J	1375 mW	11.0 mW/°C	880 mW	275 mW

[‡] This is the inverse of the traditional junction-to-ambient thermal resistance (R_{θJA}). Thermal resistances are not production tested and the values given are for informational purposes only.

recommended operating conditions

		MIN	NOM	MAX	UNIT
V _{CC}	Supply voltage	AM26LS31C	4.75	5	5.25
		AM26LS31M	4.5	5	5.5
V _{IH}	High-level input voltage		2		V
V _{IL}	Low-level input voltage			0.8	V
I _{OH}	High-level output current			-20	mA
I _{OL}	Low-level output current			20	mA
T _A	Operating free-air temperature	AM26LS31C	0	70	°C
		AM26LS31M	-55	125	



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AM26LS31C, AM26LS31M
QUADRUPLE DIFFERENTIAL LINE DRIVER

SLLS114H – JANUARY 1979 – REVISED JULY 2002

electrical characteristics over operating free-air temperature range (unless otherwise noted)†

PARAMETER	TEST CONDITIONS		MIN	TYP‡	MAX	UNIT
V_{IK} Input clamp voltage	$V_{CC} = \text{MIN}$, $I_I = -18 \text{ mA}$				-1.5	V
V_{OH} High-level output voltage	$V_{CC} = \text{MIN}$, $I_{OH} = -20 \text{ mA}$	$T_A = -55^\circ\text{C}$	2.4			V
		All other temperatures	2.5			
V_{OL} Low-level output voltage	$V_{CC} = \text{MIN}$, $I_{OL} = 20 \text{ mA}$			0.5		V
I_{OZ} Off-state (high-impedance-state) output current	$V_{CC} = \text{MIN}$	$V_O = 0.5 \text{ V}$			-20	μA
		$V_O = 2.5 \text{ V}$			20	
I_I Input current at maximum input voltage	$V_{CC} = \text{MAX}$, $V_I = 7 \text{ V}$			0.1		mA
I_{IH} High-level input current	$V_{CC} = \text{MAX}$, $V_I = 2.7 \text{ V}$			20		μA
I_{IL} Low-level input current	$V_{CC} = \text{MAX}$, $V_I = 0.4 \text{ V}$			-0.36		mA
I_{OS} Short-circuit output current§	$V_{CC} = \text{MAX}$			-30	-150	mA
I_{CC} Supply current	$V_{CC} = \text{MAX}$, All outputs disabled		32	80		mA

† For C suffix devices, V_{CC} MIN = 4.75 V and V_{CC} MAX = 5.25 V. For M suffix devices, V_{CC} MIN = 4.5 V and V_{CC} MAX = 5.5 V.

‡ All typical values are at $V_{CC} = 5 \text{ V}$ and $T_A = 25^\circ\text{C}$.

§ Not more than one output should be shorted at a time, and duration of the short circuit should not exceed one second.

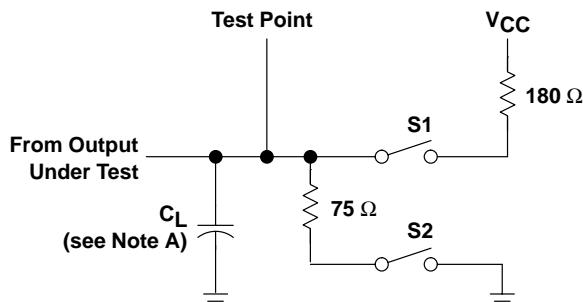
switching characteristics, $V_{CC} = 5 \text{ V}$, $T_A = 25^\circ\text{C}$ (see Figure 1)

PARAMETER	TEST CONDITIONS		MIN	TYP	MAX	UNIT
t_{PLH} Propagation delay time, low-to-high-level output	$C_L = 30 \text{ pF}$, S1 and S2 open			14	20	ns
t_{PHL} Propagation delay time, high-to-low-level output				14	20	
t_{PZH} Output enable time to high level	$C_L = 30 \text{ pF}$	$R_L = 75 \Omega$		25	40	ns
t_{PZL} Output enable time to low level		$R_L = 180 \Omega$		37	45	
t_{PHZ} Output disable time from high level	$C_L = 10 \text{ pF}$, S1 and S2 closed			21	30	ns
t_{PLZ} Output disable time from low level				23	35	
Output-to-output skew	$C_L = 30 \text{ pF}$, S1 and S2 open		1	6		ns

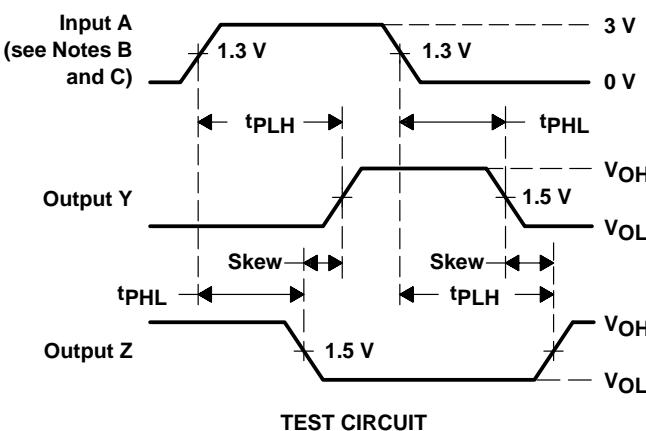
AM26LS31C, AM26LS31M QUADRUPLE DIFFERENTIAL LINE DRIVER

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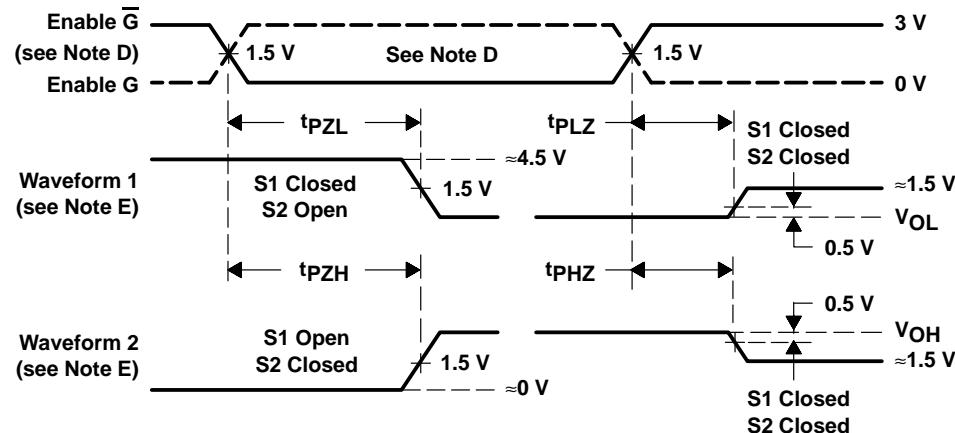
PARAMETER MEASUREMENT INFORMATION



PROPAGATION DELAY TIMES AND SKEW



TEST CIRCUIT



ENABLE AND DISABLE TIME WAVEFORMS

- NOTES:
- C_L includes probe and jig capacitance.
 - All input pulses are supplied by generators having the following characteristics: PRR ≤ 1 MHz, $Z_O \approx 50 \Omega$, $t_r \leq 15$ ns, $t_f \leq 6$ ns.
 - When measuring propagation delay times and skew, switches S1 and S2 are open.
 - Each enable is tested separately.
 - Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control.

Figure 1. Test Circuit and Voltage Waveforms

TYPICAL CHARACTERISTICS

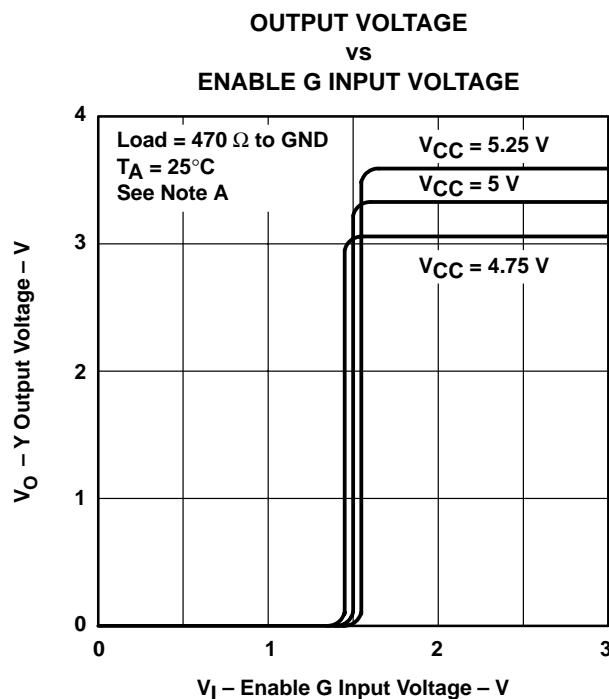


Figure 2

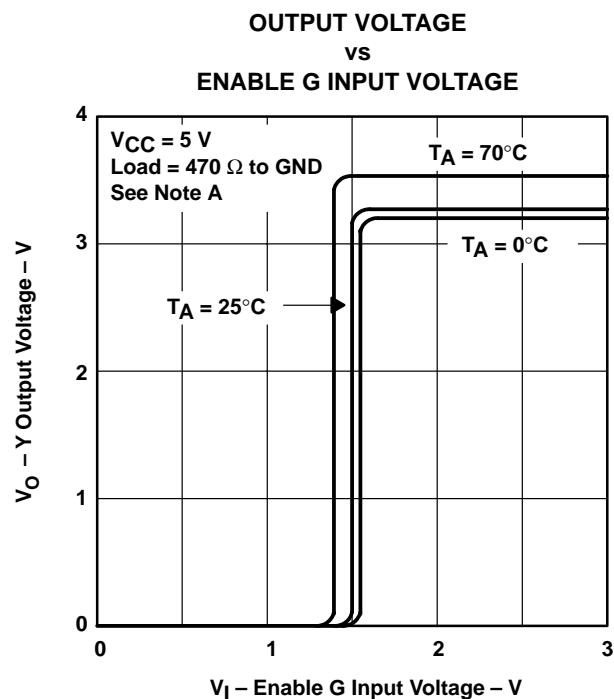


Figure 3

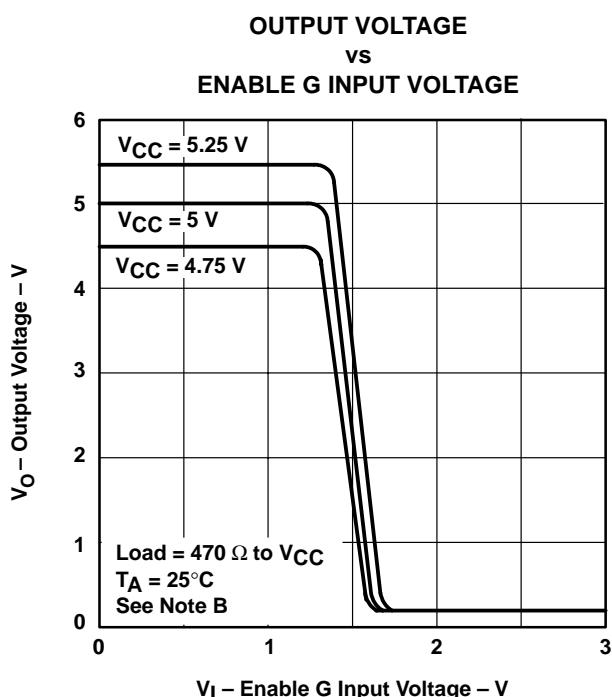


Figure 4

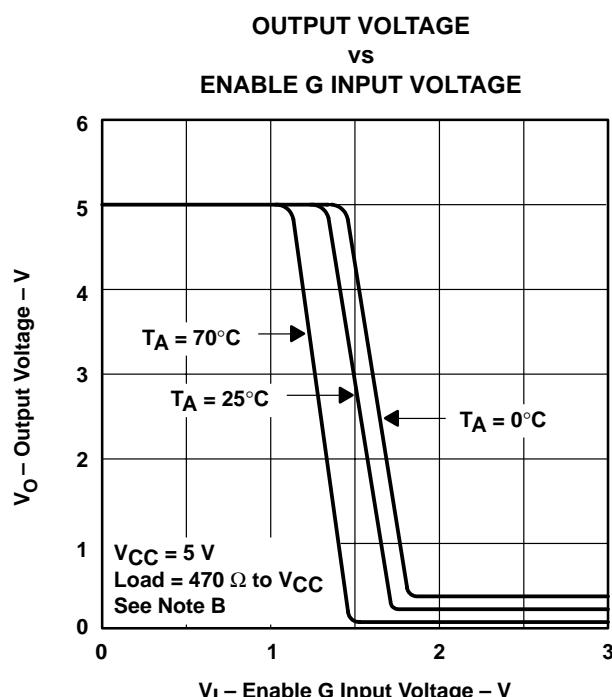


Figure 5

- NOTES: A. The A input is connected to V_{CC} during testing of the Y outputs and to ground during testing of the Z outputs.
B. The A input is connected to ground during testing of the Y outputs and to V_{CC} during testing of the Z outputs.

AM26LS31C, AM26LS31M QUADRUPLE DIFFERENTIAL LINE DRIVER

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TYPICAL CHARACTERISTICS

HIGH-LEVEL OUTPUT VOLTAGE
vs
FREE-AIR TEMPERATURE

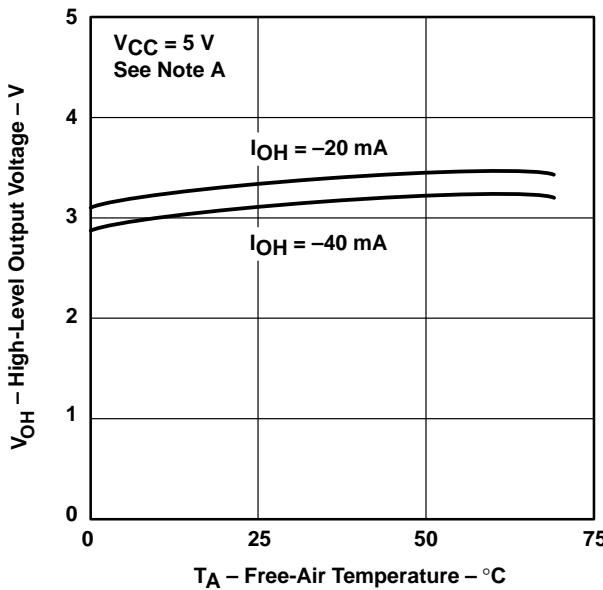


Figure 6

HIGH-LEVEL OUTPUT VOLTAGE
vs
HIGH-LEVEL OUTPUT CURRENT

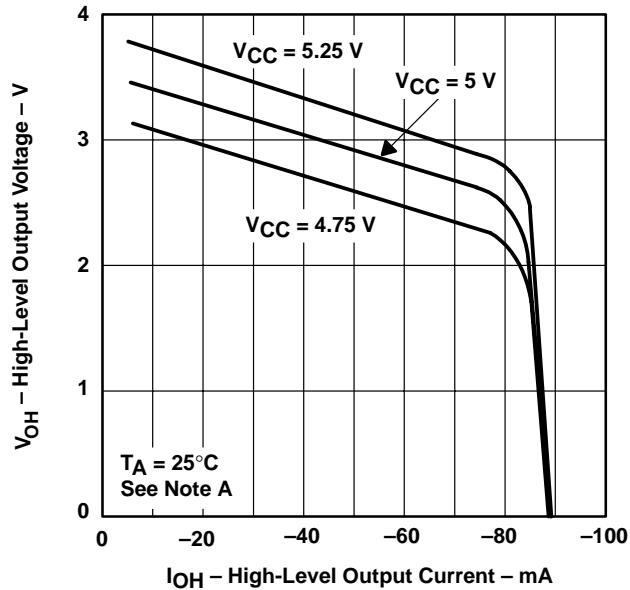


Figure 7

LOW-LEVEL OUTPUT VOLTAGE
vs
FREE-AIR TEMPERATURE

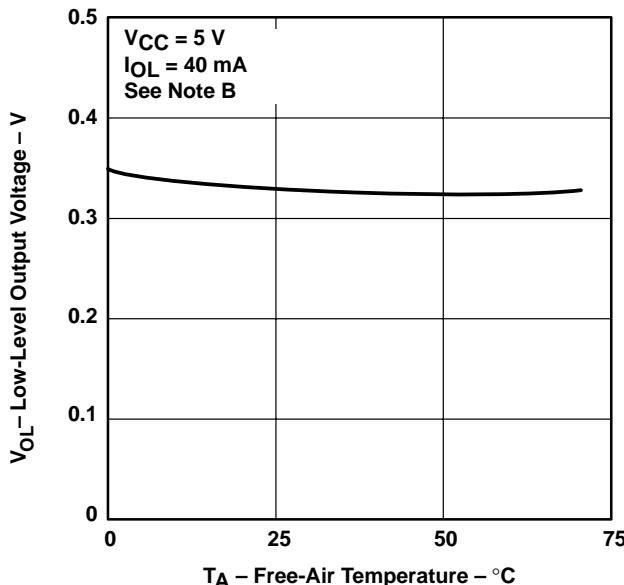


Figure 8

LOW-LEVEL OUTPUT VOLTAGE
vs
LOW-LEVEL OUTPUT CURRENT

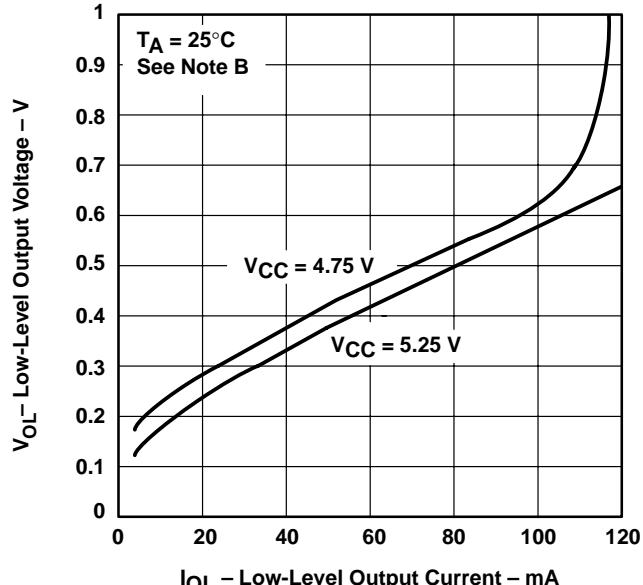


Figure 9

- NOTES: A. The A input is connected to V_{CC} during testing of the Y outputs and to ground during testing of the Z outputs.
B. The A input is connected to ground during testing of the Y outputs and to V_{CC} during testing of the Z inputs.

TYPICAL CHARACTERISTICS

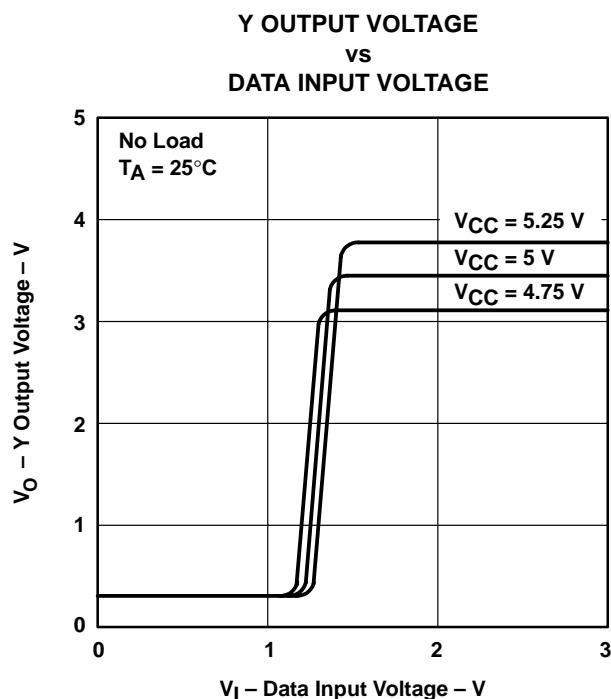


Figure 10

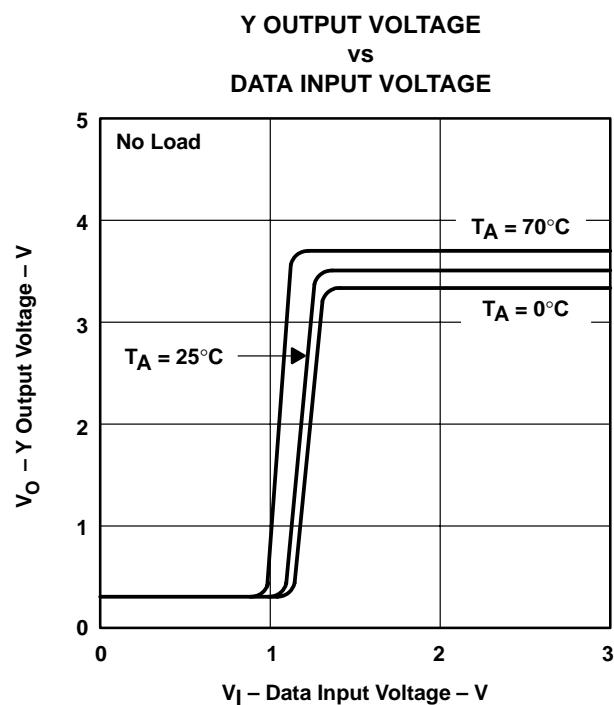


Figure 11

PACKAGING INFORMATION

Orderable Device	Status ⁽¹⁾	Package Type	Package Drawing	Pins	Package Qty	Eco Plan ⁽²⁾	Lead/Ball Finish	MSL Peak Temp ⁽³⁾
AM26LS31CD	ACTIVE	SOIC	D	16	40	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
AM26LS31CDBR	ACTIVE	SSOP	DB	16	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
AM26LS31CDBRE4	ACTIVE	SSOP	DB	16	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
AM26LS31CDE4	ACTIVE	SOIC	D	16	40	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
AM26LS31CDR	ACTIVE	SOIC	D	16	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
AM26LS31CDRE4	ACTIVE	SOIC	D	16	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
AM26LS31CN	ACTIVE	PDIP	N	16	25	Pb-Free (RoHS)	CU NIPDAU	Level-NC-NC-NC
AM26LS31CNSR	ACTIVE	SO	NS	16	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
AM26LS31CNSRG4	ACTIVE	SO	NS	16	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM

⁽¹⁾ The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

⁽²⁾ Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS) or Green (RoHS & no Sb/Br) - please check <http://www.ti.com/productcontent> for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

⁽³⁾ MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

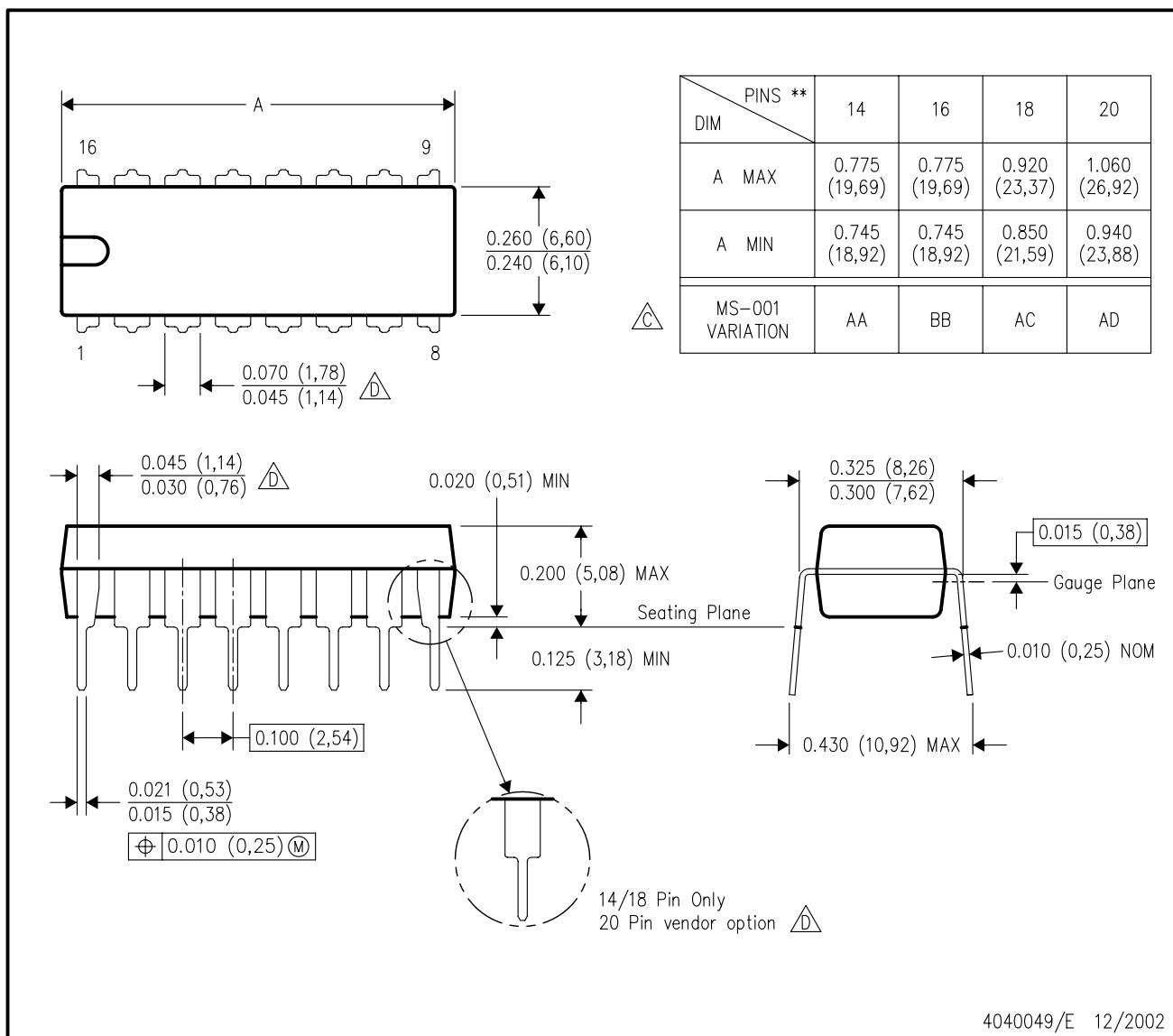
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N (R-PDIP-T**)

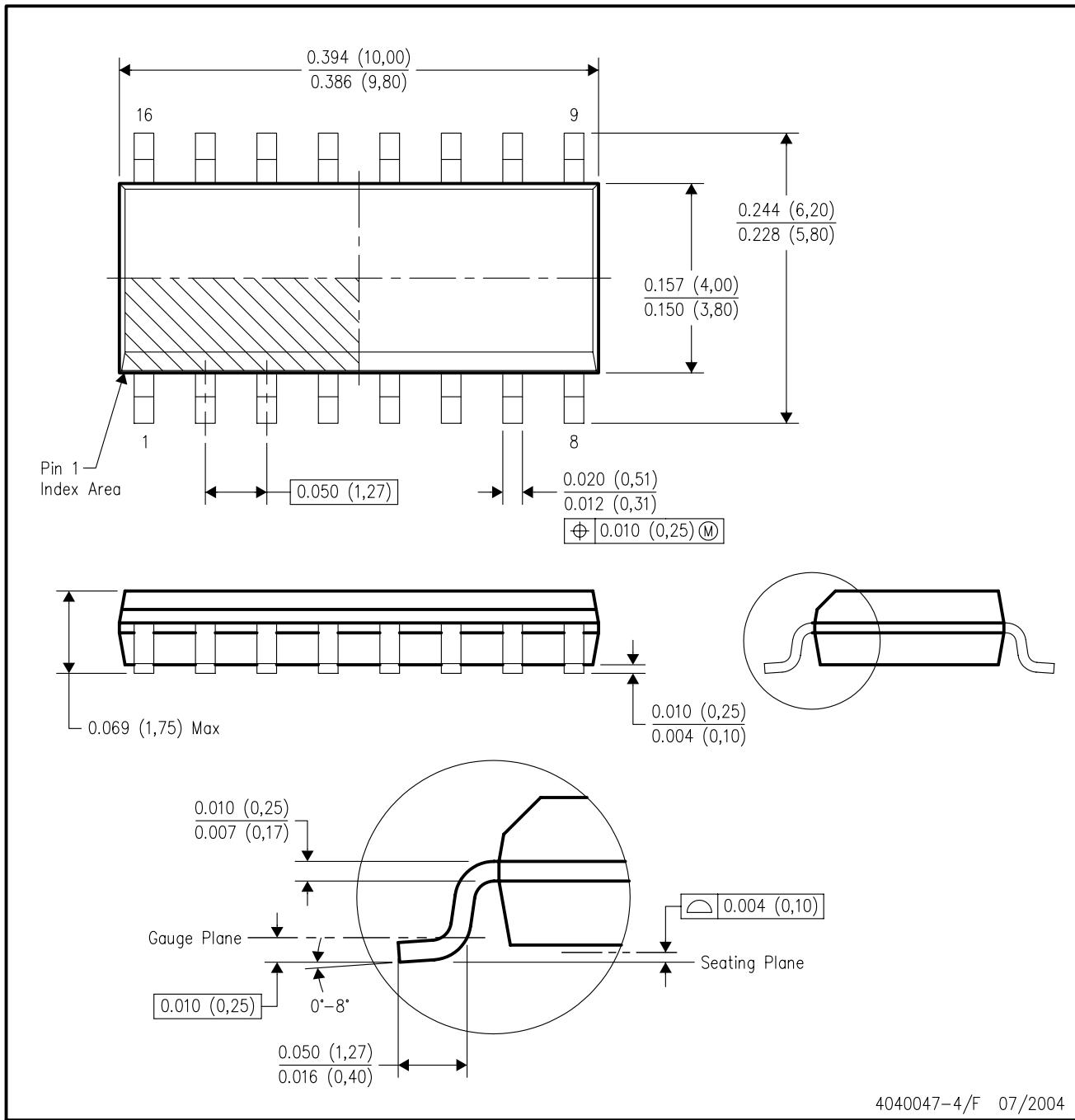
16 PINS SHOWN

PLASTIC DUAL-IN-LINE PACKAGE



D (R-PDSO-G16)

PLASTIC SMALL-OUTLINE PACKAGE



4040047-4/F 07/2004

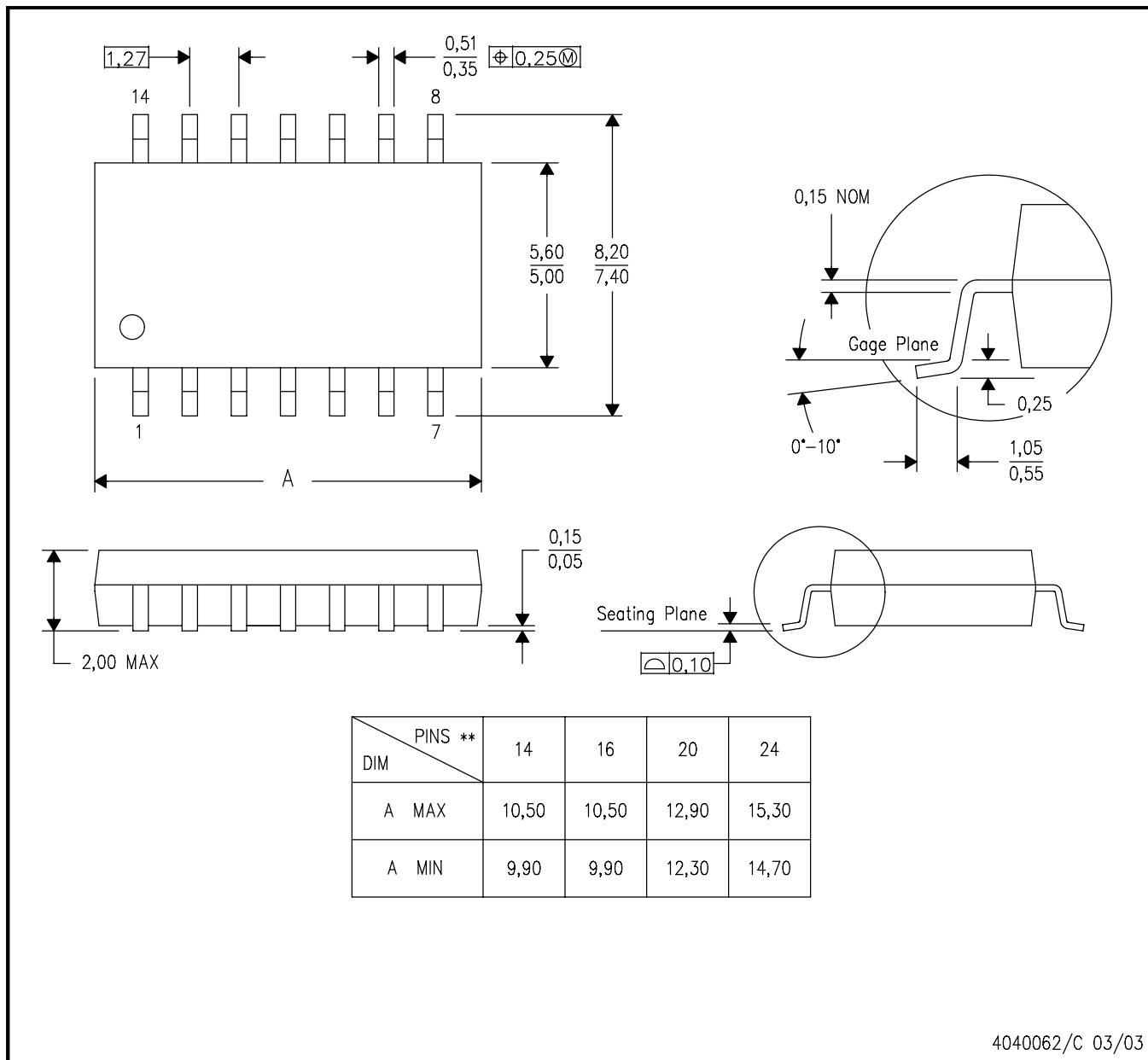
- NOTES:
- All linear dimensions are in inches (millimeters).
 - This drawing is subject to change without notice.
 - Body dimensions do not include mold flash or protrusion not to exceed 0.006 (0,15).
 - Falls within JEDEC MS-012 variation AC.

MECHANICAL DATA

NS (R-PDSO-G)**

14-PINS SHOWN

PLASTIC SMALL-OUTLINE PACKAGE

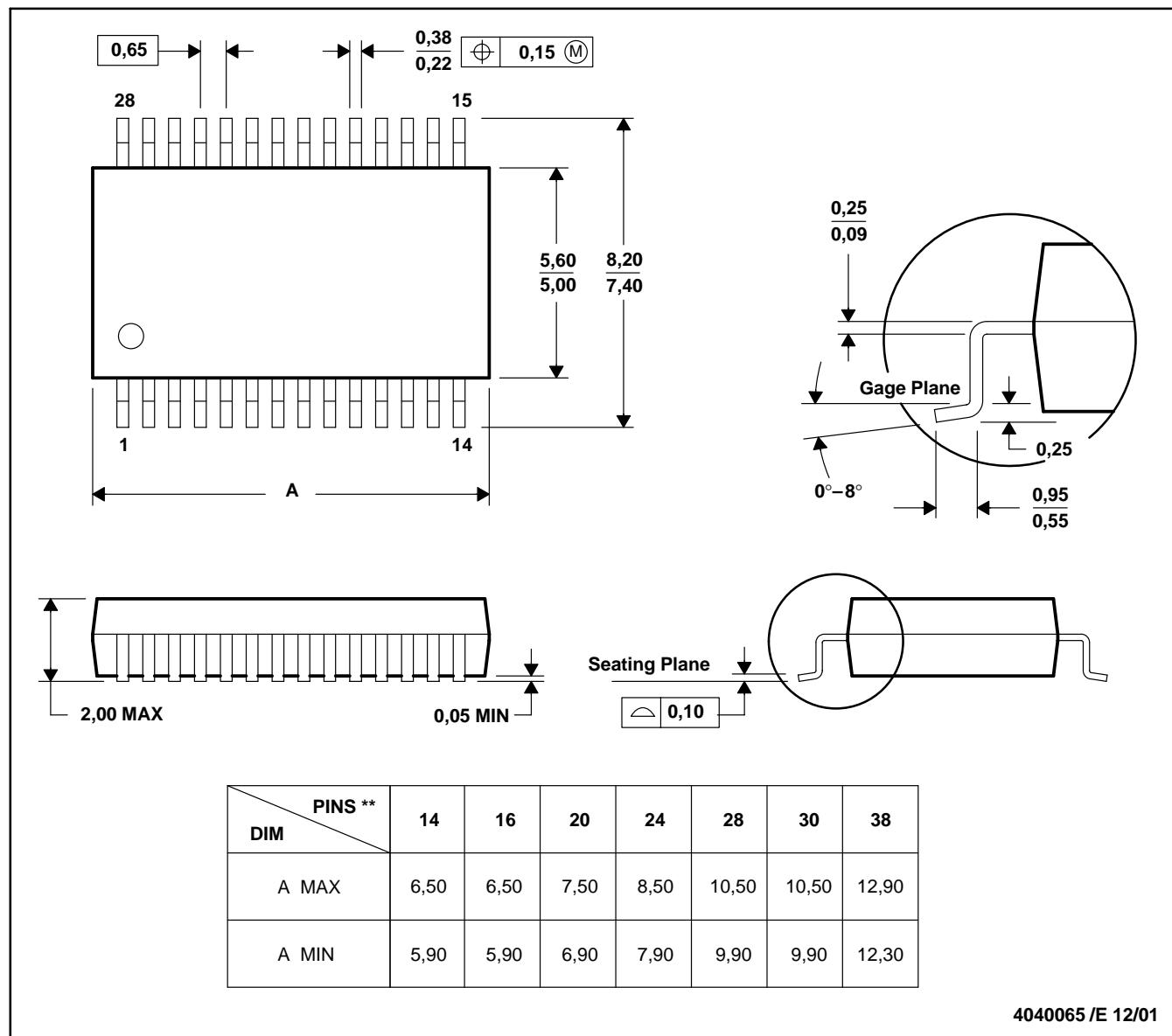


- NOTES: A. All linear dimensions are in millimeters.
 B. This drawing is subject to change without notice.
 C. Body dimensions do not include mold flash or protrusion, not to exceed 0,15.

DB (R-PDSO-G**)

PLASTIC SMALL-OUTLINE

28 PINS SHOWN



- NOTES:
- A. All linear dimensions are in millimeters.
 - B. This drawing is subject to change without notice.
 - C. Body dimensions do not include mold flash or protrusion not to exceed 0,15.
 - D. Falls within JEDEC MO-150

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View ROHS Compliant Devices

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 clear gif**AM26LS31, Status: ACTIVE**
Quadruple Differential Line Driver Features Quality & Pb-Free Data Related Products Tools & Software Samples Pricing/Packaging Inventory Symbols/Footprints Technical Documents Applications Notes Simulation Models Reference Designs**Refine Your Selection**

- Selection Guides
- Analog & Mixed-Signal

Support

- KnowledgeBase
- Contact Technical Support
- TI Cross Reference
- Training
- Part Marking Lookup

Datasheet

Download Datasheet Quadruple Differential Line Driver (Rev. H) (am26ls31.pdf, 406 KB)
12 Jul 2002 Download

**AM26LS31**

Drivers Per Package	4
Supply Voltage(s)(V)	5
ESD(kV)	2
Signaling Rate(Mbps)	10
ICC(Max)(mA)	80
Footprint	AM26LS31
Temp Range(C)	-55 to 125, 0 to 70
Pin/Package	16PDIP,16SO,16SOIC,16SSOP
Approx. 1KU Price (US\$)	.45
	Samples
	Inventory

Product Information Features

Save this to your personal library

Meets or Exceeds the Requirements of ANSI TIA/EIA-422-B and ITU Recommendation V.11

Operates From a Single 5-V Supply

TTL Compatible

Complementary Outputs

High Output Impedance in Power-Off Conditions

Complementary Output-Enable Inputs

 Description

The AM26LS31 is a quadruple complementary-output line driver designed to meet the requirements of ANSI TIA/EIA-422-B and ITU (formerly CCITT) Recommendation V.11. The 3-state outputs have high-current capability for driving balanced lines such as twisted-pair or parallel-wire transmission lines, and they are in the high-impedance state in the power-off condition. The enable function is common to all four drivers and offers the choice of an active-high or active-low enable (G, G\l) input. Low-power Schottky circuitry reduces power consumption without sacrificing speed.

Pricing/Packaging/CAD Design Tools/Samples

			Price	Packaging			CAD Design Tools		Samples
Device	Status	Temp (°C)	Budget Price (\$US) QTY	Industry Standard (TI Pkg) Pins	Top Side Marking	Standard Pack Quantity	Symbols	Footprints	Samples
AM26LS31CD	ACTIVE	0 to 70	0.45 1KU	SOIC (D) 16	View	40	<input type="checkbox"/>	<input type="checkbox"/>	Purchase Samples
AM26LS31CDBR	ACTIVE	0 to 70	0.45 1KU	SSOP (DB) 16	View	2000	<input type="checkbox"/>	<input type="checkbox"/>	Purchase Samples
AM26LS31CDBRE4	ACTIVE	0 to 70	0.45 1KU	SSOP (DB) 16	View	2000	<input type="checkbox"/>	<input type="checkbox"/>	Purchase Samples
AM26LS31CDE4	ACTIVE	0 to 70	0.45 1KU	SOIC (D) 16	View	40	<input type="checkbox"/>	<input type="checkbox"/>	Purchase Samples
AM26LS31CDR	ACTIVE	0 to 70	0.45 1KU	SOIC (D) 16	View	2500	<input type="checkbox"/>	<input type="checkbox"/>	Contact TI Distributor or Sales Office
AM26LS31CDRE4	ACTIVE	0 to 70	0.45 1KU	SOIC (D) 16	View	2500	<input type="checkbox"/>	<input type="checkbox"/>	Request Free Samples
AM26LS31CN	ACTIVE	0 to 70	0.45 1KU	PDIP (N) 16	View	25	<input type="checkbox"/>	<input type="checkbox"/>	Contact TI Distributor or Sales Office
AM26LS31CNSR	ACTIVE	0 to 70	0.61 1KU	SO (NS) 16	View	2000	<input type="checkbox"/>	<input type="checkbox"/>	Purchase Samples
AM26LS31CNSRG4	ACTIVE	0 to 70	0.50 1KU	SO (NS) 16	View	2000	<input type="checkbox"/>	<input type="checkbox"/>	Purchase Samples

Inventory

		TI Inventory Status			Reported Distributor Inventory					
AM26LS31CD		As of 9:57 AM GMT, 29 Nov 2005			As of 9:57 AM GMT, 29 Nov 2005				View all Distributors	
		In Stock	In Progress QTY Date	Lead Time	Region	Company	In Stock	Purchase	Choose a Region	!
		93*	1300 21 Dec	4 Weeks	Americas	DigiKey	>1k	<input type="text"/>		
			>10k 28 Dec			Newark InOne	708	<input type="text"/>		
						Abacus Polar	302	<input type="text"/>		
						Arrow Northern Europe	>1k	<input type="text"/>		
						Arrow Southern Europe	>1k	<input type="text"/>		
						Avnet-SILICA	>1k	<input type="text"/>		
						EBV Elektronik	>1k	<input type="text"/>		
AM26LS31CDBR		As of 9:57 AM GMT, 29 Nov 2005			As of 9:57 AM GMT, 29 Nov 2005					
		In Stock	In Progress QTY Date	Lead Time	Region	Company	In Stock	Purchase		
		0*	5842 11 Jan	8 Weeks	None Reported View Distributors					
			>10k 25 Jan							
AM26LS31CDBRE4		As of 9:57 AM GMT, 29 Nov 2005			As of 9:57 AM GMT, 29 Nov 2005					
		In Stock	In Progress QTY Date	Lead Time	Region	Company	In Stock	Purchase		
		0*	5842 11 Jan	8 Weeks	None Reported View Distributors					
			>10k 25 Jan							
AM26LS31CDE4		As of 9:57 AM GMT, 29 Nov 2005			As of 9:57 AM GMT, 29 Nov 2005					
		In Stock	In Progress QTY Date	Lead Time	Region	Company	In Stock	Purchase		
		93*	1300 21 Dec	4 Weeks	None Reported View Distributors					
			>10k 28 Dec							
AM26LS31CDR		As of 9:57 AM GMT, 29 Nov 2005			As of 9:57 AM GMT, 29 Nov 2005					

	In Stock	In Progress QTY Date	Lead Time	Region	Company	In Stock	Purchase
AM26LS31CDRE4	>10k*	>10k 28 Dec	4 Weeks	Americas	DigiKey	>1k	[]
					Arrow Southern Europe	>1k	[]
					Avnet-SILICA	>1k	[]
					EBV Elektronik	>1k	[]
AM26LS31CN	As of 9:57 AM GMT, 29 Nov 2005			As of 9:57 AM GMT, 29 Nov 2005			
	In Stock	In Progress QTY Date	Lead Time	Region	Company	In Stock	Purchase
AM26LS31CNSR	58*	>10k 26 Jan	8 Weeks	Americas	Avnet	>1k	[]
					DigiKey	>1k	[]
					Newark InOne	>1k	[]
				Europe	Abacus Polar	900	[]
					Arrow Northern Europe	>1k	[]
					Arrow Southern Europe	24	[]
					Avnet-SILICA	700	[]
					EBV Elektronik	>1k	[]
					Spoerle	>1k	[]
AM26LS31CNSRG4	As of 9:57 AM GMT, 29 Nov 2005			As of 9:57 AM GMT, 29 Nov 2005			
	In Stock	In Progress QTY Date	Lead Time	Region	Company	In Stock	Purchase
AM26LS31CDR	0*	6000 12 Dec	4 Weeks	None Reported	View Distributors		
AM26LS31CDBR				As of 9:57 AM GMT, 29 Nov 2005			
	In Stock	In Progress QTY Date	Lead Time	Region	Company	In Stock	Purchase
AM26LS31CDRE4				As of 9:57 AM GMT, 29 Nov 2005			
AM26LS31CDE4				As of 9:57 AM GMT, 29 Nov 2005			
AM26LS31CDR				As of 9:57 AM GMT, 29 Nov 2005			
AM26LS31CDRE4				As of 9:57 AM GMT, 29 Nov 2005			
AM26LS31CN				As of 9:57 AM GMT, 29 Nov 2005			
AM26LS31CNSR				As of 9:57 AM GMT, 29 Nov 2005			
AM26LS31CNSRG4				As of 9:57 AM GMT, 29 Nov 2005			

* Our information is updated daily, so please check back with us soon if this does not meet your needs. You may also contact your [TI Authorized Distributor](#), including those [listed above](#), for real time stock information.

** Lead time information is not available at this time. However, our information is updated daily so please check back with us soon. Please contact your preferred [TI Authorized Distributor](#) for additional information.

Quality & Lead (Pb)-Free Data

	Product Content					MTBF/FIT Rate
Device	Eco Plan*		Lead/Ball Finish	MSL Rating/Peak Reflow	Details	Details
AM26LS31CD <input type="checkbox"/>	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	View	View	
AM26LS31CDBR <input type="checkbox"/>	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	View	View	
AM26LS31CDBRE4 <input type="checkbox"/>	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	View	View	
AM26LS31CDE4 <input type="checkbox"/>	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	View	View	
AM26LS31CDR <input type="checkbox"/>	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	View	View	
AM26LS31CDRE4 <input type="checkbox"/>	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	View	View	
AM26LS31CN <input type="checkbox"/>	Pb-Free (RoHS)	CU NIPDAU	Level-NC-NC-NC	View	View	
AM26LS31CNSR <input type="checkbox"/>	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	View	View	
AM26LS31CNSRG4 <input type="checkbox"/>	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	View	View	

* The planned eco-friendly classification: Pb-Free (RoHS) or Green (RoHS & no Sb/Br) - please click on the Product Content Details "View" link in the table above for the latest availability information and additional product content details.

If the information you are requesting is not available online at this time, contact one of our [Product Information Centers](#) regarding the availability of this information.

Technical Documents

Datasheets

[Keep track of what's new](#)

Quadruple Differential Line Driver (Rev. H) ([am26ls31.pdf](#), 406 KB)

12 Jul 2002 [Download](#)

Application Notes

[View Application Notes for RS-422](#)

More Literature

Standard Linear Products Cross-Reference (Rev. C) ([slyt017c.pdf](#), 632 KB)

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