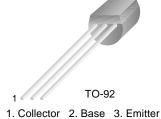


## BC327/328

## **Switching and Amplifier Applications**

- Suitable for AF-Driver stages and low power output stages
- Complement to BC337/BC338



## **PNP Epitaxial Silicon Transistor**

## Absolute Maximum Ratings Ta=25°C unless otherwise noted

Symbol	Parameter	Value	Units
V <sub>CES</sub>	Collector-Emitter Voltage		
	: BC327	-50	V
	: BC328	-30	V
V <sub>CEO</sub>	Collector-Emitter Voltage		
	: BC327	-45	V
	: BC328	-25	V
$V_{EBO}$	Emitter-Base Voltage	-5	V
Ic	Collector Current (DC)	-800	mA
P <sub>C</sub>	Collector Power Dissipation	625	mW
T <sub>J</sub>	Junction Temperature	150	°C
T <sub>STG</sub>	Storage Temperature	-55 ~ 150	°C

## Electrical Characteristics T<sub>a</sub>=25°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
BV <sub>CEO</sub>	Collector-Emitter Breakdown Voltage	I <sub>C</sub> = -10mA, I <sub>B</sub> =0				
	: BC327		-45			V
	: BC328		-25			V
BV <sub>CES</sub>	Collector-Emitter Breakdown Voltage	I <sub>C</sub> = -0.1mA, V <sub>BE</sub> =0				
	: BC327		-50			V
	: BC328		-30			V
BV <sub>EBO</sub>	Emitter-Base Breakdown Voltage	I <sub>E</sub> = -10μA, I <sub>C</sub> =0	-5			V
I <sub>CES</sub>	Collector Cut-off Current					
	: BC327	$V_{CE} = -45V, V_{BE} = 0$		-2	-100	nA
	: BC328	$V_{CE}$ = -25V, $V_{BE}$ =0		-2	-100	nA
h <sub>FE1</sub>	DC Current Gain	V <sub>CE</sub> = -1V, I <sub>C</sub> = -100mA	100		630	
h <sub>FE2</sub>		$V_{CE} = -1V, I_{C} = -300 \text{mA}$	40			
V <sub>CE</sub> (sat)	Collector-Emitter Saturation Voltage	I <sub>C</sub> = -500mA, I <sub>B</sub> = -50mA			-0.7	V
V <sub>BE</sub> (on)	Base-Emitter On Voltage	V <sub>CE</sub> = -1V, I <sub>C</sub> = -300mA			-1.2	V
f <sub>T</sub>	Current Gain Bandwidth Product	V <sub>CE</sub> = -5V, I <sub>C</sub> = -10mA, f=20MHz		100		MHz
C <sub>ob</sub>	Output Capacitance	V <sub>CB</sub> = -10V, I <sub>E</sub> =0, f=1MHz		12		pF

## **h**<sub>FE</sub> Classification

Classification	16	25	40
h <sub>FE1</sub>	100 ~ 250	160 ~ 400	250 ~ 630
h <sub>FE2</sub>	60-	100-	170-

# **Typical Characteristics**

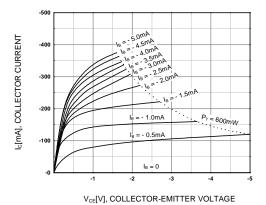
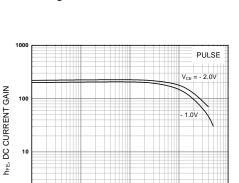


Figure 1. Static Characteristic



Ic[mA], COLLECTOR CURRENT Figure 3. DC current Gain

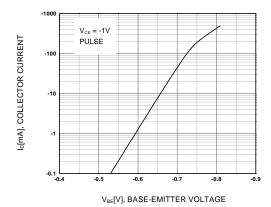
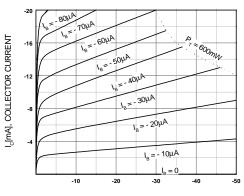


Figure 5. Base-Emitter On Voltage



 $\mathbf{V}_{\text{CE}}[\mathbf{V}]\text{, COLLECTOR-EMITTER VOLTAGE}$ 

Figure 2. Static Characteristic

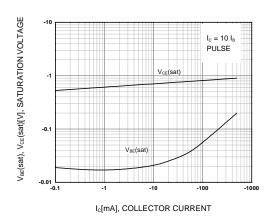


Figure 4. Base-Emitter Saturation Voltage Collector-Emitter Saturation Voltage

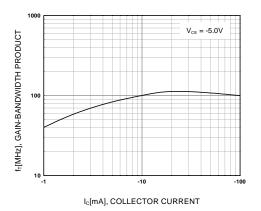
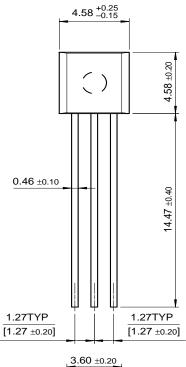


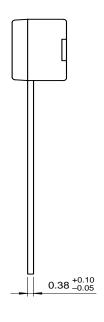
Figure 6. Gain Bandwidth Product

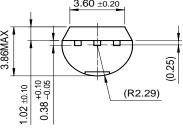
©2002 Fairchild Semiconductor Corporation Rev. B1, August 2002

# **Package Dimensions**

TO-92







#### **TRADEMARKS**

The following are registered and unregistered trademarks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks.

$ACEx^{TM}$	FACT™	ImpliedDisconnect™	PACMAN™	SPM <sup>TM</sup>
ActiveArray™	FACT Quiet series™	ISOPLANAR™	POP™	Stealth™
Bottomless™	FAST <sup>®</sup>	LittleFET™	Power247™	SuperSOT™-3
CoolFET™	FASTr™	MicroFET™	PowerTrench <sup>®</sup>	SuperSOT™-6
CROSSVOLT™	FRFET™	MicroPak™	QFET™	SuperSOT™-8
DOME™	GlobalOptoisolator™	MICROWIRE™	$QS^{TM}$	SyncFET™
EcoSPARK™	GTO™	MSX™	QT Optoelectronics™	TinyLogic™
E <sup>2</sup> CMOS™	HiSeC™	MSXPro™	Quiet Series™	TruTranslation™
EnSigna™	$I^2C^{TM}$	$OCX^{TM}$	RapidConfigure™	UHC™
Across the board.	. Around the world.™	OCXPro™	RapidConnect™	UltraFET <sup>®</sup>
The Power Franchise™		OPTOLOGIC <sup>®</sup>	SILENT SWITCHER®	$VCX^{TM}$
Programmable Ad	ctive Droop™	OPTOPLANAR™	SMART START™	

### **DISCLAIMER**

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

### LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION.

As used herein:

- 1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in significant injury to the user.
- 2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

### **PRODUCT STATUS DEFINITIONS**

### **Definition of Terms**

Datasheet Identification	Product Status	Definition
Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
Obsolete	Not In Production	This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only.